## Computer Memory Test Labs Final Report

<table>
<thead>
<tr>
<th>MOTHERBOARD</th>
<th>MANUFACTURER</th>
<th>CHIPSET</th>
<th>TEST NUMBER</th>
<th>TEST STATUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>DQ57TM</td>
<td>Intel</td>
<td>Intel® Q57</td>
<td>017696</td>
<td>PASSED</td>
</tr>
</tbody>
</table>

### Module Information

<table>
<thead>
<tr>
<th>Manufacturer Name: Dataram</th>
<th>Module ID: DAT586</th>
<th>Compatibility Ranking Level: Certified CRL 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Part #: DTM64303C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>MB: 1GB</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Config: 128M x 64</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DRAM: Hynix</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DRAM Part #: H5TC1G83TFR-H9A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PCB Part: 40096A rev A</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PCB_layer_count: 6 Layer</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Module Info: Unbuffered, Non-ECC</td>
<td>1333MHz, DDR3 DIMM</td>
<td></td>
</tr>
</tbody>
</table>

### System Information

#### Minimum System Information

- Minimum System: DQ57TM
- Serial Number: BTTM94500090
- Board Revision: E70931-301
- Post Memory Count: 2048MB
- Processor Code: Inter® Xeon® X3470, @ 2.93GHz, 4.8GT
- BIOS at time of test: TMIBX10H.86A.0025
- MCFT Memory Count: 2026M 468K

#### Maximum System Information

- Minimum System: DQ57TM
- Serial Number: BTTM94500091
- Board Revision: E70931-301
- Post Memory Count: 4096MB
- Processor Code: Inter® Xeon® X3470, @ 2.93GHz, 4.8GT
- BIOS at time of test: TMIBX10H.86A.0025
- MCFT Memory Count: 4G 512M

### Testing Details

- 55°C Temperature: Standard Voltage
- Start Date: 9/29/2010
- Stop Date: 9/30/2010
- Insertion Test: SPD Check: Pass
- MCFT Min: Pass
- MCFT Max: Pass
- S3 Test: Pass
- Power Cycle Test: N/A
- Power Cycle Time: N/A
- OS: N/A
- Service Pack: N/A
- Test Version: RST Pro3 v3.02

### Vendor Contact Information

- Contact: Lyuda Kushleyeva
- Contact Phone: (609) 799-0071 x 34
- Purchase Order #: Advanced

### Test Traveler Notes

Min configuration Test Time 24hr, 14 Loops/13 Pass, 0 Error
Max configuration Test Time 24hr, 7 Loops/7 Pass, 0 Error

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v.x.xx), XLINEAR® (v.x.xx), FXLINEAR® (v.x.xx), Patin® (v.x.xx), WinMTA® (v.x.xx). (Voltage marganing applied only to those systems that are applicable.)

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