




# Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>																										
DP55SB	Intel	Intel® P55	017109	PASSED																										
<b>MODULE INFORMATION</b>				<b>Compatibility Ranking Level</b>																										
Manufacturer Name: <b>Kingston</b>		Module ID: <b>KIN283</b>		<table style="width: 100%;"> <tr> <td style="text-align: center;"><b>Certified</b></td> <td style="text-align: center;"><b>CRL</b></td> </tr> <tr> <td style="text-align: center;"></td> <td style="text-align: center;"><b>2</b></td> </tr> </table>	<b>Certified</b>	<b>CRL</b>		<b>2</b>																						
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Part #: <b>KVR1066D3N7/1G</b>		Cas Latency: <b>7</b>																												
MB: <b>1GB</b>		Dual Die Package: <input type="checkbox"/>																												
Config: <b>128M x 64</b>		Env. Code <b>RoHS</b>																												
DRAM: <b>Kingston</b>		Bank: <b>1</b>																												
DRAM Part #: <b>D1288JEKAPGA7U rev A</b>		Assembly Type: <b>Single Board/Non Stacked</b>																												
PCB Part: <b>2025402-0F1.C00 rev C, R/C A rev 0</b>		<b>DDR3 DIMM</b>																												
PCB_layer_count: <b>6 Layer</b>																														
Module Info: <b>Unbuffered Non-ECC 1066MHz</b>																														
<b>SYSTEM INFORMATION</b>		<b>TESTING DETAILS</b>																												
<b>MINIMUM SYSTEM INFORMATION</b>		<table style="width: 100%;"> <tr> <td colspan="2">55°C Temperature: Standard Voltage</td> </tr> <tr> <td>Start Date:</td> <td>9/8/2009</td> </tr> <tr> <td>Stop Date:</td> <td>9/9/2009</td> </tr> <tr> <td>Insertion Test:</td> <td></td> </tr> <tr> <td>SPD Check:</td> <td>Pass</td> </tr> <tr> <td>MCFT Min:</td> <td>Pass</td> </tr> <tr> <td>MCFT Max:</td> <td>Pass</td> </tr> <tr> <td>S3 Test:</td> <td>Pass</td> </tr> <tr> <td>Power Cycle Test:</td> <td>N/A</td> </tr> <tr> <td>Power Cycle Time:</td> <td></td> </tr> <tr> <td>OS:</td> <td>N/A</td> </tr> <tr> <td>Service Pack:</td> <td>N/A</td> </tr> <tr> <td>Test Version:</td> <td>RST Pro3 v3.02</td> </tr> </table>			55°C Temperature: Standard Voltage		Start Date:	9/8/2009	Stop Date:	9/9/2009	Insertion Test:		SPD Check:	Pass	MCFT Min:	Pass	MCFT Max:	Pass	S3 Test:	Pass	Power Cycle Test:	N/A	Power Cycle Time:		OS:	N/A	Service Pack:	N/A	Test Version:	RST Pro3 v3.02
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OS:	N/A																													
Service Pack:	N/A																													
Test Version:	RST Pro3 v3.02																													
Minimum System:	DP55SB																													
Serial Number:	AZSB92900028																													
Board Revision:	E47220-302																													
Post Memory Count:	2048MB																													
Processor Code:	Intel® Core™ CPU 860 @ 2.80GHz, 64 A																													
Bios at time of test:	KGIBX10J.86A.2924.2009.0721.1849																													
MCFT Memory Count:	2013M 224K																													
Max_HDD_Man:																														
Max_HDD_Cap:																														
Max_HDD_Ser:																														
<b>MAXIMUM SYSTEM INFORMATION</b>																														
Maximum System:	DP55SB																													
Serial Number:	AZSB9290000R																													
Board Revision:	E47220-302																													
Post Memory Count:	4096MB																													
Processor Code:	Intel® Core™ CPU 860 @ 2.80GHz, 64 A																													
Bios at time of test:	KGIBX10J.86A.2924.2009.0721.1849																													
MCFT Memory Count:	4G 768M																													
Max_HDD_Man:																														
Max_HDD_Cap:																														
Max_HDD_Ser:																														
<b>VENDOR CONTACT INFORMATION</b>																														
Contact:	Contact Phone:	Purchase Order #:																												
Mike Mohney	714.424.3976																													
Test Traveler Notes:			CMTL USE ONLY:																											
			Verified by:																											
			Emailed by:																											

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility, Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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