

Computer Memory Test Labs Final Report

| <u>MOTHERBOARD</u> | <u>MANUFACTURER</u> | <u>CHIPSET</u> | <u>TEST NUMBER</u> | <u>TEST STATUS</u> | | | | |
|---|----------------------------------|--|--|---|-----------|-----|---|---|
| D975XBX2/S975XBX2 | Intel | Intel® 975X | 016292 | PASSED | | | | |
| <u>MODULE INFORMATION</u> | | | | <u>Compatibility Ranking Level</u> | | | | |
| Manufacturer Name: Buffalo | | Module ID: BUF247 | | <table style="width: 100%;"> <tr> <td style="font-weight: bold;">Certified</td> <td style="font-weight: bold;">CRL</td> </tr> <tr> <td style="text-align: center;"></td> <td style="text-align: center; font-size: 24pt; font-weight: bold;">9</td> </tr> </table> | Certified | CRL |  | 9 |
| Certified | CRL | | | | | | | |
|  | 9 | | | | | | | |
| Part #: | D2U800C-S1GECJ | | | | | | | |
| MB: | 1GB | | | | | | | |
| Config: | 128M x 64 | | Cas Latency: 5 | | | | | |
| DRAM: | Elpida | | Dual Die Package: <input type="checkbox"/> | | | | | |
| DRAM Part #: | E1108ACBG-8E-E rev C | | Env. Code RoHS | | | | | |
| PCB Part: | 2DUD18F-AA rev A | | Bank: 1 | | | | | |
| PCB_layer_count: | 6 Layer | Assembly Type: Single Board/Non Stacked | | | | | | |
| Module Info: | Unbuffered | Non-ECC | 800Mhz | DDR2 DIMM | | | | |
| <u>SYSTEM INFORMATION</u> | | <u>TESTING DETAILS</u> | | | | | | |
| <u>MINIMUM SYSTEM INFORMATION</u> | | | | | | | | |
| Minimum System: | D975XBX2/S975XBX2 | 55°C Temperature: Standard Voltage | | | | | | |
| Serial Number: | BQB26390003W | | | | | | | |
| Board Revision: | D53350-400 | Start Date: | 1/27/2009 | | | | | |
| Post Memory Count: | 1024 | Stop Date: | 1/28/2009 | | | | | |
| Processor Code: | Intel® P4,3.6GHz,800FSB | Insertion Test: | | | | | | |
| Bios at time of test: | BX97520J.86A.2813.2008.0114.2256 | SPD Check: | Pass | | | | | |
| MCFT Memory Count: | 1020M 788K | MCFT Min: | Pass | | | | | |
| Max_HDD_Man: | | MCFT Max: | Pass | | | | | |
| Max_HDD_Cap: | | S3 Test: | Pass | | | | | |
| Max_HDD_Ser: | | Power Cycle Test: | N/A | | | | | |
| <u>MAXIMUM SYSTEM INFORMATION</u> | | Power Cycle Time: | | | | | | |
| Maximum System: | D975XBX2/S975XBX2 | OS: | Windows XP | | | | | |
| Serial Number: | BQB26390004L | Service Pack: | 2 | | | | | |
| Board Revision: | D53350-400 | Test Version: | R.S.T PRO v2.81 | | | | | |
| Post Memory Count: | 4096 | | | | | | | |
| Processor Code: | Intel® P4,3.6GHz,800FSB | | | | | | | |
| Bios at time of test: | BX97520J.86A.2813.2008.0114.2256 | | | | | | | |
| MCFT Memory Count: | 4G 768M | | | | | | | |
| Max_HDD_Man: | | | | | | | | |
| Max_HDD_Cap: | | | | | | | | |
| Max_HDD_Ser: | | | | | | | | |
| <u>VENDOR CONTACT INFORMATION</u> | | | | | | | | |
| Contact: | Bruce Reilly | Contact Phone: | Purchase Order #: | | | | | |
| Test Traveler Notes: | | | CMTL USE ONLY: | | | | | |
| | | | Verified by: | | | | | |
| | | | Emailed by: | | | | | |

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility, Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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