




Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
SB5000P	Corvalent	Intel® 5000P	016058	PASSED

<u>MODULE INFORMATION</u>		<u>Compatibility Ranking Level</u>				
Manufacturer Name: Viking	Module ID: VIK233	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Certified</td> <td style="text-align: center;">CRL</td> </tr> <tr> <td style="text-align: center;"></td> <td style="text-align: center;">5</td> </tr> </table>	Certified	CRL		5
Certified	CRL					
	5					
Part #: VR5EF567218FBWL2						
MB: 2GB						
Config: 256M x 72						
DRAM: Hynix						
DRAM Part #: HY5PS1G831CFP-Y5 rev C						
PCB Part: D2F28B rev B						
PCB_layer_count: 6 Layer						
Module Info: Buffered ECC 667Mhz FB-DIMM						
Cas Latency: 5						
Assembly Type: Single Board/Non Stacked	Dual Die Package: <input type="checkbox"/>					
Low Profile: Yes	Env. Code RoHS					
Register Part #:	Bank: 2					
PLL Part #:						

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<p><u>MINIMUM SYSTEM INFORMATION</u></p> <p>Minimum System: SB5000P</p> <p>Board Rev</p> <p>Serial Number: 0951190014</p> <p>Post Memory Count: 2048MB</p> <p>Processor Code: Intel® Xeon®, X5130, 2.0GHz,1333FSB</p> <p>Bios at time of test: Ver 08.00.14</p> <p>MCFT Memory Count: 2047M 952K</p> <p><u>MAXIMUM SYSTEM INFORMATION</u></p> <p>Maximum System: SB5000P</p> <p>Board Rev</p> <p>Serial Number: 0951190014</p> <p>Post Memory Count: 16384MB</p> <p>Processor Code: Intel® Xeon®, X5130, 2.0GHz,1333FSB</p> <p>Bios at time of test: Ver 08.00.14</p> <p>MCFT Memory Count: 16G 512M</p>	<p>50°C Temperature: Standard Voltage</p> <p>Start Date: 12/10/2008</p> <p>Stop Date: 12/11/2008</p> <p>Insertion Test: Pass</p> <p>SPD Check: Pass</p> <p>MCFT Min: N/A</p> <p>MCFT Max: Pass</p> <p>Software Version: RST PRO3 v2.82</p> <p>Power Cycling Status: N/A</p> <p>Power Cycling Time:</p>

VENDOR CONTACT INFORMATION

Contact: Adrian Proctor	Contact Phone: (949) 643-7255	Purchase Order #: AP
-------------------------	-------------------------------	----------------------

Test Traveler Notes:	CMTL USE ONLY:
----------------------	----------------

	Verified by:
	Emailed by:

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.xx), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.xx), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

WWW.CMTLABS.COM