

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
(DG33BU)/DP35DP/DG33FB	Intel	Intel® G33	015514	PASSED
MODULE INFORMATION				
Manufacturer Name: Buffalo Part #: D2U800C-1GEJJ MB: 1GB Cas Latency: 5 Config: 128M x 64 Assembly Type: Single Board/Non Stacked DRAM: Elpida DRAM Part #: E5108AJBG-8E-E rev J PCB Part: 2DUE28F-AA na PCB_layer_count: 6 Layer Module Info: Unbuffered Non-ECC 800Mhz DDR2 DIMM				
SYSTEM INFORMATION			TESTING DETAILS	
MINIMUM SYSTEM INFORMATION Minimum System: (DG33BU)/DP35DP/DG33FB Serial Number: AZBU71800727 Board Revision: D79951-405 Post Memory Count: 1024 Processor Code: Intel® Core™ 6420, 2.13GHz, 1.06GHz F Bios at time of test: DPP3510J.86A.0297.2007.1022.2244 MCFT Memory Count: 1004M 128K Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser: MAXIMUM SYSTEM INFORMATION Maximum System: (DG33BU)/DP35DP/DG33FB Serial Number: AZBU718007TN Board Revision: D79951-405 Post Memory Count: 4096 Processor Code: Intel® Core™ 6420, 2.13GHz, 1.06GHz F Bios at time of test: DPP3510J.86A.0297.2007.1022.2244 MCFT Memory Count: 4G 704M Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser:			55°C Temperature: Standard Voltage Start Date: 6/23/2008 Stop Date: 6/24/2008 Insertion Test: SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: Pass Power Cycle Test: N/A Power Cycle Time: OS: Service Pack: N/A Test Version: R.S.T PRO v2.81	
VENDOR CONTACT INFORMATION				
Contact: Bruce Reilly		Contact Phone:		Purchase Order #:
Test Traveler Notes:			CMTL USE ONLY:	
			Verified by:	
			Emailed by:	

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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