




Computer Memory Test Labs Final Report

| <u>MOTHERBOARD</u> | <u>MANUFACTURER</u> | <u>CHIPSET</u> | <u>TEST NUMBER</u> | <u>TEST STATUS</u> | | |
|--|---------------------|--|--|--|----------------------|---|
| D975XBX2/S975XBX2 | Intel | Intel® 975X | 014460 | PASSED | | |
| MODULE INFORMATION | | | | Compatibility Ranking Level | | |
| Manufacturer Name: Smart Modular Technologies Part #: SG2566UDR212851ME MB: 2GB Config: 256M x 64 DRAM: Micron DRAM Part #: MT47H128M8HQ-25E rev E PCB Part: PG58G240LUBUB1RE rev B PCB_layer_count: 8 Layer Module Info: Unbuffered Non-ECC 800Mhz | | | Cas Latency: 5 Dual Die Package: <input type="checkbox"/> Env. Code RoHS Bank: 2 Assembly Type: Single Board/Non Stacked DDR2 DIMM | <table border="1" style="margin: auto;"> <tr> <td style="text-align: center;">Certified CRL</td> </tr> <tr> <td style="text-align: center;"> 13</td> </tr> </table> | Certified CRL |  13 |
| Certified CRL | | | | | | |
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| SYSTEM INFORMATION | | TESTING DETAILS | | | | |
| MINIMUM SYSTEM INFORMATION Minimum System: D975XBX2/S975XBX2 Serial Number: BQB26390003W Board Revision: D53350-400 Post Memory Count: 2048 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: BX97520J.86A.2792.2007.0905.1804 MCFT Memory Count 2044M 788K Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser: | | 55°C Temperature: Standard Voltage Start Date: 10/19/2007 Stop Date: 10/22/2007 Insertion Test: SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: Pass Power Cycle Test: Pass Power Cycle Time: 36 OS: Windows XP Service Pack: 2 Test Version: RST PRO V2.71 | | | | |
| MAXIMUM SYSTEM INFORMATION Maximum System: D975XBX2/S975XBX2 Serial Number: BQB26390004L Board Revision: D53350-400 Post Memory Count: 8192 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: BX97520J.86A.2792.2007.0905.1804 MCFT Memory Count 8G 768M Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser: | | | | | | |
| VENDOR CONTACT INFORMATION | | | | | | |
| Contact: Jim Schwarz | | Contact Phone: | | Purchase Order #: 10042007 | | |
| Test Traveler Notes: | | | CMTL USE ONLY: | | | |
| | | | Verified by: | | | |
| | | | Emailed by: | | | |

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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