




Computer Memory Test Labs Final Report

| <u>MOTHERBOARD</u> | <u>MANUFACTURER</u> | <u>CHIPSET</u> | <u>TEST NUMBER</u> | <u>TEST STATUS</u> | | |
|--|---------------------|--|--|--|----------------------|---|
| DG33TL | Intel | Intel® G33 | 014052 | PASSED | | |
| MODULE INFORMATION | | | | Compatibility Ranking Level | | |
| Manufacturer Name: Smart Modular Technologies Part #: SG1286UDR264851NB MB: 1GB Config: 128M x 64 DRAM: Nanya DRAM Part #: NT5TU64M8BE-25C rev B PCB Part: PG58G240LUBUB1RE rev B PCB_layer_count: 8 Layer Module Info: Unbuffered Non-ECC 800Mhz | | | Cas Latency: 5 Dual Die Package: <input type="checkbox"/> Env. Code RoHS Bank: 2 Assembly Type: Single Board/Non Stacked DDR2 DIMM | <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Certified CRL</td> </tr> <tr> <td style="text-align: center;"> 11</td> </tr> </table> | Certified CRL |  11 |
| Certified CRL | | | | | | |
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| SYSTEM INFORMATION | | TESTING DETAILS | | | | |
| MINIMUM SYSTEM INFORMATION Minimum System: DG33TL Serial Number: BQTL7130000T Board Revision: D89517-300 Post Memory Count: 1024 Processor Code: Intel® Core™ 2 Duo CPU E6850 @ 3. Bios at time of test: DPP3510J.86A.0246.2007.0613.1659 MCFT Memory Count 1006M Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser: | | 55°C Temperature: Standard Voltage Start Date: 7/24/2007 Stop Date: 7/25/2007 Insertion Test: SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: Pass Power Cycle Test: Pass Power Cycle Time: 35 OS: Windows XP Service Pack: 2 Test Version: RST PRO V2.56 | | | | |
| MAXIMUM SYSTEM INFORMATION Maximum System: DG33TL Serial Number: BQTL7130000U Board Revision: D89517-300 Post Memory Count: 4096 Processor Code: Intel® Core™ 2 Duo CPU E6850 @ 3. Bios at time of test: DPP3510J.86A.0246.2007.0613.1659 MCFT Memory Count 4G 704M Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser: | | | | | | |
| VENDOR CONTACT INFORMATION | | | | | | |
| Contact: Jim Schwarz | | Contact Phone: | | Purchase Order #: 07/03/2007 | | |
| Test Traveler Notes: | | | CMTL USE ONLY: | | | |
| | | | Verified by: | | | |
| | | | Emailed by: | | | |

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.xx), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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