




Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>		
DQ965WC/DG965PZ	Intel	Intel® G965	012344	PASSED		
MODULE INFORMATION				Compatibility Ranking Level		
Manufacturer Name: Smart Modular Technologies Part #: SG1286UDR64861-IB MB: 1GB Config: 128M x 64 DRAM: Qimonda (Infineon) DRAM Part #: HYB18T512800BF25 rev B PCB Part: K0602 (240-9-1) PCB_layer_count: 8 Layer Module Info: Unbuffered Non-ECC 800Mhz			Cas Latency: 6 Dual Die Package: <input type="checkbox"/> Env. Code RoHS Bank: 2 Assembly Type: Single Board/Non Stacked DDR2 DIMM	<table border="1" style="margin: auto;"> <tr> <td style="padding: 5px;">  </td> <td style="padding: 5px; text-align: center;"> Certified CRL 9 </td> </tr> </table>		Certified CRL 9
	Certified CRL 9					
SYSTEM INFORMATION		TESTING DETAILS				
MINIMUM SYSTEM INFORMATION Minimum System: DQ965WC/DG965PZ Serial Number: LAPZ6260003J Board Revision: D41835-300 Post Memory Count: 1024 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: MQ96510J.86A.1458 MCFT Memory Count: 1005M 488K Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser:		55°C Temperature: Standard Voltage Start Date: 11/3/2006 Stop Date: 11/6/2006 Insertion Test: Pass SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: N/A Power Cycle Test: N/A Power Cycle Time: OS: N/A Service Pack: N/A Test Version: R.S.T PRO V2.56				
MAXIMUM SYSTEM INFORMATION Maximum System: DQ965WC/DG965PZ Serial Number: LAPZ62600054 Board Revision: D41835-300 Post Memory Count: 4096 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: MQ96510J.86A.1458 MCFT Memory Count: 4G 704M Max_HDD_Man: Max_HDD_Cap: Max_HDD_Ser:						
VENDOR CONTACT INFORMATION						
Contact: Jim Schwarz		Contact Phone:		Purchase Order #: 09/13/2006		
Test Traveler Notes:			CMTL USE ONLY:			
			Verified by:			
			Emailed by:			

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.xx), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

WWW.CMTLABS.COM