




Computer Memory Test Labs : Intel Advanced Final Report

| <u>MOTHERBOARD</u> | <u>MANUFACTURER</u> | <u>CHIPSET</u> | <u>TEST NUMBER</u> | <u>TEST STATUS</u> | | | | |
|---|---|--|--------------------|--|-----------|-----|---|---|
| SE7320EP2 | Intel | Intel E7320 | 011675 | PASSED | | | | |
| <u>MODULE INFORMATION</u> | | | | <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Certified</td> <td style="text-align: center;">CRL</td> </tr> <tr> <td style="text-align: center;"></td> <td style="text-align: center; font-size: 24pt;">2</td> </tr> </table> | Certified | CRL |  | 2 |
| Certified | CRL | | | | | | | |
|  | 2 | | | | | | | |
| Manufacturer Name: TRS | | Cas Latency: 3 | | | | | | |
| Part #: TRS31260X | | Assembly: Single Board/Non St | | | | | | |
| MB: 512MB | | Low Profile: Yes | | | | | | |
| Config: 64M x 72 | | Register Part #: PI74SSTUA32864NB | | | | | | |
| DRAM: Infineon | | PLL Part #: PI6CU877NFE | | | | | | |
| DRAM Part #: HYB18T512800AF5 rev A | | Dual Die Package Support <input type="checkbox"/> Bank 1 | | | | | | |
| PCB Part: M0551LA1 rev 1 | | Environment Code | | RoHS | | | | |
| PCB_layer_count: 6 Layer | | | | | | | | |
| Module Info: Registered ECC | | 400Mhz | | DDR2 DIM | | | | |
| <u>PRE-TESTING INFORMATION</u> | | | | | | | | |
| <u>MINIMUM SYSTEM INFORMATION</u> | | <u>MAXIMUM SYSTEM INFORMATION</u> | | | | | | |
| Board Revision: D11950-302 | Minimum System: SE7320EP2 | Maximum System: SE7320EP2 | | | | | | |
| Insertion Test: Pass | Serial Number: QSEP51600165 | Serial Number: QSEP51600213 | | | | | | |
| SPD Status: Pass | Bios at time of test: SE7320EP20.86B.P.04.00.001E | Bios at time of test: SE7320EP20.86B.P.04.00.001E | | | | | | |
| Power Cycle Stat: Pass | Processor Code: Xeon 3.6Ghz 800Mhz x 2 | Processor Code: Xeon 3.6Ghz 800Mhz x 2 | | | | | | |
| Power Cycle Time: 55 | Memory Count : 1024 | Memory Count : 2048 | | | | | | |
| | Event Log Verify: Clear | Event Log Verify: Clear | | | | | | |
| | OS Memory Count: 1GB | OS Memory Count: 2GB | | | | | | |
| | OS/Service Pack: Win2003 Server | OS/Service Pack: Win2003 Server NA | | | | | | |
| | Disk Man: Hitachi | Disk Man: Hitachi | | | | | | |
| | Disk Capacity: 82.3GB | Disk Capacity: 82.2GB | | | | | | |
| | Disk Serial #: S2RJBBVB | Disk Serial #: J2RJ9BMB | | | | | | |
| <u>TESTING DETAILS FOR PHASE 1</u> | | <u>TESTING DETAILS FOR PHASE 2</u> | | <u>TESTING DETAILS FOR PHASE 3</u> | | | | |
| Ambient Temperature : Standard Voltage | | 55°C Temperature : 1.69 (- 6 %) Volts | | 55°C Temperature : 1.90 (+ 6 %) Volts | | | | |
| Start Date: 6/23/06 | | Start Date: 6/26/06 | | Start Date: 6/26/06 | | | | |
| Stop Date: 6/26/06 | | Stop Date: 6/27/06 | | Stop Date: 6/27/06 | | | | |
| Min MCFT Test Status: Pass | | Min WINPIE Test Status: Pass | | Min WINPIE Test Status: Pass | | | | |
| Max MCFT Test Status: Pass | | Max WINPIE Test Status: Pass | | Max WINPIE Test Status: Pass | | | | |
| SEL/Outrider Min: | | SEL/Outrider Min: Pass | | SEL/Outrider Min: Pass | | | | |
| SEL/Outrider Max: | | SEL/Outrider Max: Pass | | SEL/Outrider Max: Pass | | | | |
| | | Summary Min: Pass | | Summary Min: Pass | | | | |
| | | Summary Max: Pass | | Summary Max: Pass | | | | |
| Ph1 Package: MCFT 2.24 | | Ph2 Package1: WinPIE 4.30 | | Ph3 Package1: WinPIE 4.30 | | | | |
| Test Phase Status: Pass | | Test Phase Status: Pass | | Test Phase Status: Pass | | | | |
| Single Bit Errors <input type="checkbox"/> | | | | | | | | |
| QTY: | | | | | | | | |
| <u>VENDOR CONTACT INFORMATION</u> | | | | | | | | |
| Contact: Guido Ries | | Contact Phone: +49 7249 910-380 | | Purchase Order #: 1.06076.99.360 | | | | |
| Test Traveler Notes: | | | | <u>CMTL USE ONLY:</u> | | | | |
| VM Data @ EEPROM # 020, 54, 002 & # 012, 52, 000. | | | | Verified by: | | | | |
| | | | | Emailed by: | | | | |

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility. Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP® Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

WWW.CMTLABS.COM