

Computer Memory Test Labs : Intel Advanced Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>	
SE7320EP2	Intel	Intel E7320	010492	PASSED	
MODULE INFORMATION					
Manufacturer Name: Smart Modular Technologies					
Part #: SG1287RDR264835IA					
MB:	1GB	Cas Latency:	3		
Config:	128M x 72	Assembly:	Single Board/Non St		
DRAM:	Infineon	Low Profile:	Yes		
DRAM Part #:	HYB18T512800AF37 rev A	Register Part #:	UF32864AHLF		
PCB Part:	PG58G240NEBUB2RB rev A	PLL Part #:	97U877AHLF		
PCB_layer_count:	6 Layer	Dual Die Package Support	<input type="checkbox"/>	Bank	
Module Info:	Registered ECC	400Mhz	DDR2 DIM	Environment Code	
PRE-TESTING INFORMATION		MINIMUM SYSTEM INFORMATION		MAXIMUM SYSTEM INFORMATION	
Board Revision:	D11950-302	Minimum System:	SE7320EP2		
Insertion Test:	Pass	Serial Number:	QSEP51600165		
SPD Status:	Pass	Bios at time of test:	SE7320EP20.86B.P.04.00.001E		
Power Cycle Stat:	Pass	Processor Code:	Xeon 3.6Ghz 800Mhz x 2		
Power Cycle Time:	51	Memory Count :	2048		
		Event Log Verify:	Clear		
		OS Memory Count:	2GB		
		OS/Service Pack:	Win2003 ADV	NA	
		Disk Man:	Hitachi		
		Disk Capacity:	82.3GB		
		Disk Serial #:	S2RJBBVB		
		Maximum System:	SE7320EP2		
		Serial Number:	QSEP51600213		
		Bios at time of test:	SE7320EP20.86B.P.04.00.001E		
		Processor Code:	Xeon 3.6Ghz 800Mhz x 2		
		Memory Count :	4096		
		Event Log Verify:	Clear		
		OS Memory Count:	4GB		
		OS/Service Pack:	Win2003 ADV	NA	
		Disk Man:	Hitachi		
		Disk Capacity:	82.2GB		
		Disk Serial #:	J2RJ9BMB		
TESTING DETAILS FOR PHASE 1		TESTING DETAILS FOR PHASE 2		TESTING DETAILS FOR PHASE 3	
Ambient Temperature : Standard Voltage		55°C Temperature : 1.69 (- 6 %) Volts		55°C Temperature : 1.90 (+ 6 %) Volts	
Start Date:	02/08/06	Start Date:	02/09/06	Start Date:	02/09/06
Stop Date:	02/09/06	Stop Date:	02/10/06	Stop Date:	02/10/06
Min MCFT Test Status:	Pass	Min WINPIE Test Status:	Pass	Min WINPIE Test Status:	Pass
Max MCFT Test Status:	Pass	Max WINPIE Test Status:	Pass	Max WINPIE Test Status:	Pass
SEL/Outrider Min:		SEL/Outrider Min:	Pass	SEL/Outrider Min:	Pass
SEL/Outrider Max:		SEL/Outrider Max:	Pass	SEL/Outrider Max:	Pass
		Summary Min:	Pass	Summary Min:	Pass
		Summary Max:	Pass	Summary Max:	Pass
Ph1 Package:	MCFT 2.24	Ph2 Package1:	WinPIE 4.30	Ph3 Package1:	WinPIE 4.30
Test Phase Status:	Pass	Test Phase Status:	Pass	Test Phase Status:	Pass
Single Bit Errors <input type="checkbox"/>					
QTY:					
VENDOR CONTACT INFORMATION					
Contact:	Contact Phone:		Purchase Order #:		
Jim Schwarz			10/24/2005		
Test Traveler Notes:				CMTL USE ONLY:	
				Verified by:	
				Emailed by:	

Test Technician: Thanh Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility, Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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