

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D955XBK	Intel	Intel D955XE	009912	PASSED
MODULE INFORMATION				
Manufacturer Name: Smart Modular Technologies Part #: SG1286UDR264843IAP MB: 1GB Cas Latency: 4 Config: 128M x 64 Assembly Type: Single Board/Non Stacked DRAM: Infineon DRAM Part #: HYB18T512800AF37 rev A PCB Part: PG58G240NUBUB2RB rev A PCB_layer_count: 6 Layer Module Info: Unbuffered Non-ECC 533Mhz DDR2 DIMM				
SYSTEM INFORMATION			TESTING DETAILS	
MINIMUM SYSTEM INFORMATION Minimum System: D955XBK Serial Number: ABBK51296412 Board Revision: C96732-400 Post Memory Count: 1024 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: BK95510J.86A.1234.2005.0411.1858 MCFT Memory Count: 1023 Max_HDD_Man: Maxtor Max_HDD_Cap: 40GB Max_HDD_Ser: WMAD11743172 MAXIMUM SYSTEM INFORMATION Maximum System: D955XBK Serial Number: ABBK51296430 Board Revision: C96732-400 Post Memory Count: 4096 Processor Code: P4 3.6Ghz 800Mhz Bios at time of test: BK95510J.86A.1234.2005.0411.1858 MCFT Memory Count: 4gb Max_HDD_Man: Maxtor Max_HDD_Cap: 40GB Max_HDD_Ser: WMAD11743172			55°C Temperature: Standard Voltage Start Date: 8/12/2005 Stop Date: 8/15/2005 Insertion Test: Pass SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: Pass Power Cycle Test: Pass Power Cycle Time: 36 OS: Windows Service Pack: NA Test Version: MCFT 2.24	
VENDOR CONTACT INFORMATION				
Contact: Jim Schwarz		Contact Phone:	Purchase Order #: P-08/01/2005	
Test Traveler Notes:			CMTL USE ONLY:	
			Verified by: Emailed by:	

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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