

# Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
GLC/D865GBF/D865	Intel	Intel 865G	009648	PASSED
<b>MODULE INFORMATION</b>				
Manufacturer Name: <b>Avant Technology</b> Part #: <b>AVM6464U39C3400K8-SPA</b> MB: <b>512MB</b> Config: <b>64M x 64</b> DRAM: <b>Spectek</b> DRAM Part #: <b>S80032VMCTL-5BG rev G</b> PCB Part: <b>B6U815 1.0 rev 1</b> PCB_layer_count: <b>6 Layer</b> Module Info: <b>Unbuffered      Non-ECC      400Mhz      DDR DIMM</b> Cas Latency: <b>3</b> Assembly Type: <b>Single Board/Non Stacked</b> Low Profile: <b>Yes</b> Register Part #: PLL Part #:				
<b>SYSTEM INFORMATION</b>			<b>TESTING DETAILS</b>	
<b>MINIMUM SYSTEM INFORMATION</b> Minimum System: D865GLC/D865GBF/D865GRH 05-51 Serial Number: ABLC31832723 Post Memory Count: 496 Processor Code: P4 2.6Ghz 800Mhz Bios at time of test: BF86510A.86A.0075.P24 MCFT Memory Count: 495  <b>MAXIMUM SYSTEM INFORMATION</b> Maximum System: D865GLC/D865GBF/D865GRH 05-51 Serial Number: IWLC31000896 Post Memory Count: 2040 Processor Code: P4 2.6Ghz 800Mhz Bios at time of test: BF86510A.86A.0075.P24 MCFT Memory Count: 2039			50°C Temperature: Standard Voltage  Start Date: 6/30/2005 Stop Date: 7/1/2005 Insertion Test: Pass SPD Check: Pass MCFT Min: Pass MCFT Max: Pass Software Version: MCFT V2.24	
<b>VENDOR CONTACT INFORMATION</b>				
Contact: Dino Roseland		Contact Phone: (512) 491-7411	Purchase Order #: AV0605	
Test Traveler Notes:			CMTL USE ONLY:	
			Verified by:	
			Emailed by:	

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.xx), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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