Computer Memory Test Labs Final Report

MOTHERBOARD	MANUFACTURER	CHIPSET	TEST NUMBER	TEST STATUS
D915GEV/D915GUX/D915PCY/D915PCM/D915PBL	Intel	Intel D915	008402	PASSED

MODULE INFORMATION

Manufacturer Name: **Buffalo**
Part #: **D2U533B-1GMAJ**
MB: **1GB**
Config: **128M x 64**
DRAM: **Micron**
DRAM Part #: **MT47H64M8BT(FT)-37E rev A**
PCB Part: **2DUZ28F-AA**
PCB_layer_count: **6 Layer**
Module Info: **Unbuffered Non-ECC 533Mhz DDR2 DIMM**
Cas Latency: **4**
Assembly Type: **Single Board/Non Stacked**

SYSTEM INFORMATION

MINIMUM SYSTEM INFORMATION

Minimum System:	D915GEV/D915GUX/D915PCY/
Board Revision:	301
Post Memory Count:	1023
Processor Code:	P4 3.2Ghz 800Mhz
Bios at time of test:	EV91510A.86A.0209.2004.0526.1812
MCFT Memory Count:	1022

MAXIMUM SYSTEM INFORMATION

Maximum System:	D915GEV/D915GUX/D915PCY/
Board Revision:	301
Post Memory Count:	2936
Processor Code:	P4 3.2Ghz 800Mhz
Bios at time of test:	EV91510A.86A.0209.2004.0526.1812
MCFT Memory Count:	2935

TESTING DETAILS

55°C Temperature: Standard Voltage

Start Date:	12/2/2004
Stop Date:	12/3/2004
Insertion Test:	Pass
SPD Check:	Pass
MCFT Min:	Pass
MCFT Max:	Pass
S3 Test:	Pass
Power Cycle Test:	Pass
Power Cycle Time:	32
OS:	Windows
Service Pack:	NA
Test Version:	MCFT 1.4a

VENDOR CONTACT INFORMATION

Contact: Tim Juarez
Contact Phone:
Purchase Order #:

Test Traveler Notes:	CMTL USE ONLY:
	Verified by: Emailed by:

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage marganing applied only to those systems that are applicable.)

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