

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D915GEV/D915GUX/D915PCY/D915PCM/D915PBI	Intel	Intel D915	008104	PASSED

MODULE INFORMATION

Manufacturer Name: Smart Modular Technologies
Part #: SM1286UDR26484-3-I
MB: 1GB
Config: 128M x 64
DRAM: Infineon
DRAM Part #: HYB18T512800AF37 rev A
PCB Part: 240-2-3
PCB_layer_count: 8 Layer
Module Info: Unbuffered Non-ECC 533Mhz DDR2 DIMM
Cas Latency: 4
Assembly Type: Single Board/Non Stacked

SYSTEM INFORMATION

TESTING DETAILS

MINIMUM SYSTEM INFORMATION

Minimum System: D915GEV/D915GUX/D915PCY/
 Board Revision: C63667-301
 Post Memory Count: 1016
 Processor Code: P4 3.4Ghz 800Mhz
 Bios at time of test: EV91510A.86A.0332.2004.1201.0225
 MCFT Memory Count: 1015

MAXIMUM SYSTEM INFORMATION

Maximum System: D915GEV/D915GUX/D915PCY/
 Board Revision: C63667-301
 Post Memory Count: 3064
 Processor Code: P4 3.4Ghz 800Mhz
 Bios at time of test: EV91510A.86A.0332.2004.1201.0225
 MCFT Memory Count: 3063

55°C Temperature: Standard Voltage
 Start Date: 1/4/2005
 Stop Date: 1/5/2005
 Insertion Test: Pass
 SPD Check: Pass
 MCFT Min: Pass
 MCFT Max: Pass
 S3 Test: Pass
 Power Cycle Test: Pass
 Power Cycle Time: 35
 OS: Windows
 Service Pack: NA
 Test Version: MCFT 2.24

VENDOR CONTACT INFORMATION

Contact: Jim Schwarz **Contact Phone:** **Purchase Order #:** 092604

Test Traveler Notes:

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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