

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D915GEV/D915GUX/D915PCY/D915PCM/D915PBI	Intel	Intel D915	007756	PASSED

MODULE INFORMATION

Manufacturer Name: ATP Electronics
Part #: AJ32K64A8BQD5S
MB: 256MB
Config: 32M x 64
DRAM: Samsung
DRAM Part #: K4T56083QF-GCD5 rev F
PCB Part: SJ240A08K1
PCB_layer_count: 6 Layer
Module Info: Unbuffered Non-ECC 533Mhz DDR2 DIMM
Cas Latency: 4
Assembly Type: Single Board/Non Stacked

SYSTEM INFORMATION

TESTING DETAILS

MINIMUM SYSTEM INFORMATION

Minimum System: D915GEV/D915GUX/D915PCY/ 5-33
 Post Memory Count: 248
 Processor Code: P4 3.2GHz 800MHz
 Bios at time of test: EV91510A.86A.0209.2004.0526.1812
 MCFT Memory Count: 247

MAXIMUM SYSTEM INFORMATION

Maximum System: D915GEV/D915GUX/D915PCY/ 5-33
 Post Memory Count: 1023
 Processor Code: P4 3.2GHz 800MHz
 Bios at time of test: EV91510A.86A.0209.2004.0526.1812
 MCFT Memory Count: 1022

55°C Temperature: Standard Voltage

Start Date: 9/2/2004
 Stop Date: 9/3/2004
 Insertion Test: Pass
 SPD Check: Pass
 MCFT Min: Pass
 MCFT Max: Pass
 S3 Test: Pass
 Power Cycle Test: Pass
 Power Cycle Time: 22
 OS: Windows
 Service Pack: NA
 Test Version: MCFT 1.4a

VENDOR CONTACT INFORMATION

Contact: Louis Lau **Contact Phone:** (408) 732-5000 x 5f **Purchase Order #:**

Test Traveler Notes:

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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