

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865GLC/D865GBF/D865G RH	Intel	Intel 865G	007665	PASSED

MODULE INFORMATION

Manufacturer Name: **Legend**
 Part #: **L3264DC5-BU1HDC5D**
 MB: **256MB**
 Config: **32M x 64**
 DRAM: **Hyundai**
 DRAM Part #: **HY5DU56822DT-J rev D**
 PCB Part: **B6U808 rev 1**
 PCB_layer_count: **6 Layer**
 Module Info: **Unbuffered Non-ECC 266Mhz DDR DIMM**
 Cas Latency: **2.5**
 Assembly Type: **Single Board/Non Stacked**

SYSTEM INFORMATION

TESTING DETAILS

MINIMUM SYSTEM INFORMATION

Minimum System: La Crosse
 Post Memory Count: 255
 Processor Code: P4 2.6GHz 800MHz
 Bios at time of test: BF86510A.86A.0049.P11 5-18
 MCFT Memory Count: 254

MAXIMUM SYSTEM INFORMATION

Maximum System: La Crosse
 Post Memory Count: 1008
 Processor Code: P4 2.6GHz 800MHz
 Bios at time of test: BF86510A.86A.0049.P11 5-18
 MCFT Memory Count: 1007

55°C Temperature: Standard Voltage

Start Date: 8/27/2004
 Stop Date: 8/30/2004
 Insertion Test: Pass
 SPD Check: Pass
 MCFT Min: Pass
 MCFT Max: Pass
 S3 Test: Pass
 Power Cycle Test: Pass
 Power Cycle Time: 21

VENDOR CONTACT INFORMATION

Contact: _____ Contact Phone: _____ Purchase Order #: 311135

Test Traveler Notes:

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician: Sonni Nguyen

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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