

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D915GEV/D915GUX/D915PCY/D915PCM/D915PBI	Intel	Intel D915	006954	PASSED

MODULE INFORMATION

Manufacturer Name: **Buffalo**
 Part #: **D2U533B-512MBJ**
 MB: **512MB**
 Config: **64M x 64**
 DRAM: **Micron**
 DRAM Part #: **MT47H32M8BP(FP)-37E rev B**
 PCB Part: **2DUZ28F-AA**
 PCB_layer_count: **6 Layer**
 Module Info: **Unbuffered Non-ECC 533Mhz DDR2 DIMM**
 Cas Latency: **4**
 Assembly Type: **Single Board/Non Stacked**

SYSTEM INFORMATION

TESTING DETAILS

MINIMUM SYSTEM INFORMATION

Minimum System: Eatonville/Luxemburg
 Post Memory Count: 504
 Processor Code: P4 3.20GHz 800MHz
 Bios at time of test: EV91510A.86A.0209.2004.0526 1
 MCFT Memory Count: 504

MAXIMUM SYSTEM INFORMATION

Maximum System: Eatonville/Luxemburg
 Post Memory Count: 2040
 Processor Code: P4 3.20GHz 800MHz
 Bios at time of test: EV91510A.86A.0209.2004.0526 2
 MCFT Memory Count: 2040

55°C Temperature: Standard Voltage

Start Date: 6/21/2004
 Stop Date: 6/22/2004
 Insertion Test: Pass
 SPD Check: Pass
 MCFT Min: Pass
 MCFT Max: Pass
 S3 Test: Pass
 Power Cycle Test: Pass
 Power Cycle Time: 22

VENDOR CONTACT INFORMATION

Contact: Tim Juarez Contact Phone: Purchase Order #:

Test Traveler Notes:

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician: Tu Tong

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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