

# Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865GLC/D865GBF	Intel	La Crosse	004061	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: <b>Buffalo</b>				
Part #: <b>DD333-S256/SD</b>				
MB: <b>256MB</b>				
Config: <b>32M x 64</b>				
DRAM: <b>Samsung</b>				
DRAM Part #: <b>K4H560838D-TCB3 rev D</b>				
PCB Part: <b>UE0832-AA</b>				
PCB_layer_count: <b>6 Layer</b>				
Module Info: <b>Unbuffered      Non-ECC      333Mhz      DDR DIMM</b>				
Cas Latency: <b>2.5</b>				
Assembly Type: <b>Single Board/Non Stacked</b>				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<b><u>MINIMUM SYSTEM INFORMATION</u></b>	
Minimum System: LaCrosse	55°C Temperature: Standard Voltage
Post Memory Count: 256	Start Date: 4/2/2003
Processor Code: P4 2.66GHz	Stop Date: 4/3/2003
Bios at time of test: BF86510A.86A.0013.B04      1	Insertion Test: Pass
MCFT Memory Count: 256	SPD Check: Pass
<b><u>MAXIMUM SYSTEM INFORMATION</u></b>	MCFT Min: Pass
Maximum System: LaCrosse	MCFT Max: Pass
Post Memory Count: 1024	S3 Test: Pass
Processor Code: P4 2.66GHz	Power Cycle Test: Pass
Bios at time of test: BF86510A.86A.0013.B04      2	Power Cycle Time: 31
MCFT Memory Count: 1024	

<u>VENDOR CONTACT INFORMATION</u>	
Contact: Tim Juarez	Contact Phone:      Purchase Order #:
Test Traveler Notes:	CMTL USE ONLY:
	Verified by:
	Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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