

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D850EMD2/D850EMV2/D850 EMVR	Intel	Medford 2	004001	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: Kingston				
Part #: KVR1066X18-4/128				
MB: 128MB				
Config: 64M x 18				
DRAM: Samsung				
DRAM Part #: K4R881869D-FCT9 rev D				
PCB Part: MR18R1624AF1				
PCB_layer_count: 8 Layer				
Module Info: Rambus ECC 1066Mhz RIMM				
Cas Latency: 16K				
Assembly Type: Single Board/Non Stacked				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<u>MINIMUM SYSTEM INFORMATION</u>	
Minimum System: Medford 2	55°C Temperature: Standard Voltage
Post Memory Count: 256	Start Date: 3/14/2003
Processor Code: P4 2.53GHz	Stop Date: 3/17/2003
Bios at time of test: MV85010A.86A.0059.P21 1	Insertion Test: Pass
MCFT Memory Count: 256	SPD Check: N/A
<u>MAXIMUM SYSTEM INFORMATION</u>	MCFT Min: Pass
Maximum System: Medford 2	MCFT Max: Pass
Post Memory Count: 512	S3 Test: Pass
Processor Code: P4 2.53GHz	Power Cycle Test: Pass
Bios at time of test: MV85010A.86A.0059.P21 2	Power Cycle Time: 30
MCFT Memory Count: 512	

<u>VENDOR CONTACT INFORMATION</u>		
Contact: Don Beauchamp	Contact Phone: (714) 435-2662	Purchase Order #: See Intel
Test Traveler Notes:		<u>CMTL USE ONLY:</u>
		Verified by:
		Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

WWW.CMTLABS.COM