Computer Memory Test Labs Final Report

MOTHERBOARD	MANUFACTURER	CODE NAME	TEST NUMBER	TEST STATUS
D865GLC/D865GBF	Intel	La Crosse	003841	PASSED

MODULE INFORMATION

Manufacturer Name: **Buffalo**
Part #: **DD266-256/SE**
MB: **256MB**
Config: **32M x 64**
DRAM: **Samsung**
DRAM Part #: **K4H280838E-TCB0 rev E**
PCB Part: **UE0502-AA**
PCB_layer_count: **6 Layer**
Module Info: **Unbuffered Non-ECC 266Mhz DDR DIMM**
Cas Latency: **2.5**
Assembly Type: **Single Board/Non Stacked**

SYSTEM INFORMATION

MINIMUM SYSTEM INFORMATION

Minimum System:	LaCrosse	
Post Memory Count:	256	
Processor Code:	P4 2.66GHz	
Bios at time of test:	BF86510A.86A.0012.B03	1
MCFT Memory Count:	256	

MAXIMUM SYSTEM INFORMATION

Maximum System:	LaCrosse	
Post Memory Count:	1024	
Processor Code:	P4 2.66GHz	
Bios at time of test:	BF86510A.86A.0012.B03	2
MCFT Memory Count:	1024	

TESTING DETAILS

55°C Temperature: Standard Voltage

Start Date:	3/14/2003
Stop Date:	3/17/2003
Insertion Test:	Pass
SPD Check:	Pass
MCFT Min:	Pass
MCFT Max:	Pass
S3 Test:	Pass
Power Cycle Test:	Pass
Power Cycle Time:	41

VENDOR CONTACT INFORMATION

Contact: Tim Juarez
Contact Phone:
Purchase Order #:

Test Traveler Notes:	CMTL USE ONLY:
	Verified by: Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage marganing applied only to those systems that are applicable.)

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