

# Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865GLC/D865GBF	Intel	La Crosse	003679	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: <b>Buffalo</b>				
Part #: <b>DD266-512/MB</b>				
MB: <b>512MB</b>				
Config: <b>64M x 64</b>				
DRAM: <b>Micron</b>				
DRAM Part #: <b>46V32M8-75 rev B</b>				
PCB Part: <b>UE0502-AA</b>				
PCB_layer_count: <b>6 Layer</b>				
Module Info: <b>Unbuffered      Non-ECC      266Mhz      DDR DIMM</b>				
Cas Latency: <b>2.5</b>				
Assembly Type: <b>Single Board/Non Stacked</b>				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<p><b><u>MINIMUM SYSTEM INFORMATION</u></b></p> <p>Minimum System: LaCrosse            Post Memory Count: 512            Processor Code: P4 2.66GHz            Bios at time of test: BF86510A.86A.0012.B03      1            MCFT Memory Count: 512</p> <p><b><u>MAXIMUM SYSTEM INFORMATION</u></b></p> <p>Maximum System: LaCrosse            Post Memory Count: 2048            Processor Code: P4 2.66GHz            Bios at time of test: BF86510A.86A.0012.B03      2            MCFT Memory Count: 2048</p>	<p>55°C Temperature: Standard Voltage</p> <p>Start Date: 3/17/2003            Stop Date: 3/18/2003            Insertion Test: Pass            SPD Check: Pass            MCFT Min: Pass            MCFT Max: Pass            S3 Test: Pass            Power Cycle Test: Pass            Power Cycle Time: 40</p>

<u>VENDOR CONTACT INFORMATION</u>	
Contact: Tim Juarez	Contact Phone:      Purchase Order #:
Test Traveler Notes:	CMTL USE ONLY:
	Verified by:  Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

[WWW.CMTLABS.COM](http://WWW.CMTLABS.COM)