

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865PERL	Intel	Rock Lake	003529	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: Buffalo				
Part #: DD333-X128/SD				
MB: 128MB				
Config: 16M x 64				
DRAM: Samsung				
DRAM Part #: K4H561638D-TCB3 rev D				
PCB Part: UEO632-AA				
PCB_layer_count: 6 Layer				
Module Info: Unbuffered Non-ECC 333Mhz DDR DIMM				
Cas Latency: 2.5				
Assembly Type: Single Board/Non Stacked				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<p><u>MINIMUM SYSTEM INFORMATION</u></p> <p>Minimum System: Rock Lake Post Memory Count: 128 Processor Code: P4 2.26GHz Bios at time of test: RL86510A.86A.0015.D 1 MCFT Memory Count: 128</p> <p><u>MAXIMUM SYSTEM INFORMATION</u></p> <p>Maximum System: Rock Lake Post Memory Count: 512 Processor Code: P4 2.26GHz Bios at time of test: RL86510A.86A.0015.D 2 MCFT Memory Count: 512</p>	<p>55°C Temperature: Standard Voltage</p> <p>Start Date: 3/3/2003 Stop Date: 3/4/2003 Insertion Test: Pass SPD Check: Pass MCFT Min: Pass MCFT Max: Pass S3 Test: Pass Power Cycle Test: Pass Power Cycle Time: 26</p>

<u>VENDOR CONTACT INFORMATION</u>	
Contact: Tim Juarez	Contact Phone: Purchase Order #:
Test Traveler Notes:	CMTL USE ONLY:
	Verified by: Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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