

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865PERL	Intel	Rock Lake	003379	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: Buffalo				
Part #: DD333-512/SD				
MB: 512MB				
Config: 64M x 64				
DRAM: Samsung				
DRAM Part #: K4H560838D-TCB3 rev D				
PCB Part: UE0532-AA				
PCB_layer_count: 6 Layer				
Module Info: Unbuffered Non-ECC 333Mhz DDR DIMM				
Cas Latency: 2.5				
Assembly Type: Single Board/Non Stacked				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<u>MINIMUM SYSTEM INFORMATION</u>	
Minimum System: Rock Lake	55°C Temperature: Standard Voltage
Post Memory Count: 512	Start Date: 3/18/2003
Processor Code: P4 2.26GHz	Stop Date: 3/19/2003
Bios at time of test: RL86510A.86A.0015.D 1	Insertion Test: Pass
MCFT Memory Count: 512	SPD Check: Pass
	MCFT Min: Pass
	MCFT Max: Pass
	S3 Test: Pass
	Power Cycle Test: Pass
	Power Cycle Time: 30
<u>MAXIMUM SYSTEM INFORMATION</u>	
Maximum System: Rock Lake	
Post Memory Count: 2048	
Processor Code: P4 2.26GHz	
Bios at time of test: RL86510A.86A.0015.D 2	
MCFT Memory Count: 2048	

<u>VENDOR CONTACT INFORMATION</u>	
Contact: Tim Juarez	Contact Phone: Purchase Order #:
Test Traveler Notes:	CMTL USE ONLY:
	Verified by:
	Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

WWW.CMTLABS.COM