

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D865GLC/D865GBF	Intel	La Crosse	003298	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: Buffalo				
Part #: DD4003-S256/SD				
MB: 256MB				
Config: 32M x 64				
DRAM: Samsung				
DRAM Part #: K4H560838D-TCC4				
PCB Part: UE0832-AA				
PCB_layer_count: 6 Layer				
Module Info: Unbuffered Non-ECC 400mhz DDR DIMM				
Cas Latency: 3				
Assembly Type: Single Board/Non Stacked				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<u>MINIMUM SYSTEM INFORMATION</u>	
Minimum System: LaCrosse	55°C Temperature: Standard Voltage
Post Memory Count: 256	Start Date: 3/19/2003
Processor Code: P4 2.66GHz	Stop Date: 3/20/2003
Bios at time of test: BF86510A.86A.0013.B04 1	Insertion Test: Pass
MCFT Memory Count: 256	SPD Check: Pass
	MCFT Min: Pass
	MCFT Max: Pass
	S3 Test: Pass
	Power Cycle Test: Pass
	Power Cycle Time: 30
<u>MAXIMUM SYSTEM INFORMATION</u>	
Maximum System: LaCrosse	
Post Memory Count: 1024	
Processor Code: P4 2.66GHz	
Bios at time of test: BF86510A.86A.0013.B04 2	
MCFT Memory Count: 1024	

<u>VENDOR CONTACT INFORMATION</u>	
Contact: Tim Juarez	Contact Phone: Purchase Order #:
Test Traveler Notes:	CMTL USE ONLY:
	Verified by:
	Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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