

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D850EMD2/D850EMV2/D850 EMVP	Intel	Medford 2	003183	PASSED

MODULE INFORMATION

Manufacturer Name: **ATP Electronics**
 Part #: **A-MR16-256-CT9**
 MB: **256MB**
 Config: **128M x 16**
 DRAM: **Samsung**
 DRAM Part #: **MR16R1628AF1-CT9**
 PCB Part: **MR18R1628AF1**
 PCB_layer_count: **4 Layer**
 Module Info: **Rambus Non-ECC 1066Mhz RIMM**
 Cas Latency: **16K**
 Assembly Type: **Single Board/Non Stacked**

SYSTEM INFORMATION

TESTING DETAILS

MINIMUM SYSTEM INFORMATION

Minimum System: Medford 2
 Post Memory Count: 512
 Processor Code: P4 2.26GHz
 Bios at time of test: MV85010A.86A.0059.P21 1
 MCFT Memory Count: 512

MAXIMUM SYSTEM INFORMATION

Maximum System: Medford 2
 Post Memory Count: 1024
 Processor Code: P4 2.26GHz
 Bios at time of test: MV85010A.86A.0059.P21 2
 MCFT Memory Count: 1024

55°C Temperature: Standard Voltage

Start Date: 3/4/2003
 Stop Date: 3/5/2003
 Insertion Test: Pass
 SPD Check: Pass
 MCFT Min: Pass
 MCFT Max: Pass
 S3 Test: Pass
 Power Cycle Test: Pass
 Power Cycle Time: 30

VENDOR CONTACT INFORMATION

Contact: Edward Chu Contact Phone: (408) 732-5000 X Purchase Order #:

Test Traveler Notes:

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician: David Sandstrom

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.xx), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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