

Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>
D850EMD2/D850EMV2/D850 EMVP	Intel	Medford 2	002718	PASSED

<u>MODULE INFORMATION</u>				
Manufacturer Name: Smart Modular Technologies				
Part #: SM10661832P/512-IA				
MB: 512MB				
Config: 256M x 18				
DRAM: Samsung				
DRAM Part #: MR18R162GDF0-CT9				
PCB Part: MR18R162GMN0				
PCB_layer_count: 8 Layer				
Module Info: Rambus ECC 1066Mhz RIMM				
Cas Latency: 16K				
Assembly Type: Single Board/Non Stacked				

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<u>MINIMUM SYSTEM INFORMATION</u>	
Minimum System: Medford 2	55°C Temperature: Standard Voltage
Post Memory Count: 1024	Start Date: 2/12/2003
Processor Code: P4 2.26GHz	Stop Date: 2/13/2003
Bios at time of test: MV85010A.86A.0059.P21 1	Insertion Test: Pass
MCFT Memory Count: 1024	SPD Check: Pass
	MCFT Min: Pass
	MCFT Max: Pass
	S3 Test: Pass
	Power Cycle Test: Pass
	Power Cycle Time: 33
<u>MAXIMUM SYSTEM INFORMATION</u>	
Maximum System: Medford 2	
Post Memory Count: 2048	
Processor Code: P4 2.26GHz	
Bios at time of test: MV85010A.86A.0059.P21 2	
MCFT Memory Count: 2048	

<u>VENDOR CONTACT INFORMATION</u>		
Contact: Jim Schwarz	Contact Phone:	Purchase Order #: 01/16/2003
Test Traveler Notes:		CMTL USE ONLY:
		Verified by:
		Emailed by:

Test Technician: Keith Patton

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MStRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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