


Computer Memory Test Labs Final Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CHIPSET</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>

<u>MODULE INFORMATION</u>	<u>Compatibility Ranking Level</u>
Manufacturer Name: Part #: MB: Config: DRAM: DRAM Part #: PCB Part: PCB_layer_count: Module Info: Cas Latency: Assembly Type: Low Profile: Register Part #: PLL Part #:	<div style="border: 1px solid black; padding: 5px; display: inline-block;"> Certified CRL  </div>
Dual Die Package: <input type="checkbox"/> Env. Code Bank:	

<u>SYSTEM INFORMATION</u>	<u>TESTING DETAILS</u>
<p><u>MINIMUM SYSTEM INFORMATION</u></p> Minimum System: Board Rev Serial Number: Post Memory Count: Processor Code: Bios at time of test: MCFT Memory Count: <p><u>MAXIMUM SYSTEM INFORMATION</u></p> Maximum System: Board Rev Serial Number: Post Memory Count: Processor Code: Bios at time of test: MCFT Memory Count:	50°C Temperature: Standard Voltage Start Date: Stop Date: Insertion Test: SPD Check: MCFT Min: MCFT Max: Software Version:

VENDOR CONTACT INFORMATION

Contact:	Contact Phone:	Purchase Order #:
Test Traveler Notes:		CMTL USE ONLY:
		Verified by: Emailed by:

Test Technician:

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility. Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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