

# Computer Memory Test Labs : Failure Report

<u>MOTHERBOARD</u>	<u>MANUFACTURER</u>	<u>CODE NAME</u>	<u>TEST NUMBER</u>	<u>TEST STATUS</u>

### MODULE INFORMATION

Manufacturer Name:  
 Part #:  
 MB:  
 Config:  
 DRAM:  
 DRAM Part #:  
 PCB Part:  
 PCB\_layer\_count:  
 Module Info:  
 Cas Latency:  
 Assembly:  
 Low Profile:  
 Register Part #:  
 PLL Part #:

### FAILURE DETAILS

Fail Date: Failed Power Cycling   
Failure Code: Failed Bench   
Failed Heat   
 Failure Verification Module ID#: Failed SSU   
 Bios at time of test:

### FAILURE DESCRIPTION

### FAILURE VERIFICATION

Failure Details Attached: Yes/No

		Bench	Heat
Summary Log	Min	<input type="checkbox"/>	<input type="checkbox"/>
	Max	<input type="checkbox"/>	<input type="checkbox"/>
SSU (SEL)	Min	<input type="checkbox"/>	<input type="checkbox"/>
	Max	<input type="checkbox"/>	<input type="checkbox"/>

CMTL USE ONLY:

Verified by:

Emailed by:

Test Technician:

In the event of a module failure, CMTL is not required to provide root cause analysis. Accuracy of testing is restricted to the above mentioned module with specific DRAM, PCB and other components as listed and tested with the CMTL Memory Compatibility Functionality Test, Microsoft Advanced Server 2000®, Windows NT 4.0 Enterprise Edition®, Windows XP®, Windows 98 SE®, Dos 6.22®, MSTRESS® (v x.x), XLINEAR® (v x.xx), FXLINEAR® (v x.xx), Patin® (v x.x), WinMTA® (v x.xx). (Voltage margining applied only to those systems that are applicable.)

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